

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Application of

Stuart L. CLAASSEN et al.

Group Art Unit:

Application No.: 10/776,509

Filed: February 12, 2004

Docket No.: 118584

For: FINITE IMPULSE RESPONSE FILTER METHOD AND APPARATUS

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- ☒ 2. In accordance with 37 CFR §1.98(a)(2)(ii), copies of any U.S. patents and patent application publications are not attached.

- ☒ 3. A copy of an English-language European Search Report is attached.

Respectfully submitted,

  
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JAO:YSC/mdw  
Date: July 8, 2005

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<p>DEPOSIT ACCOUNT USE AUTHORIZATION Please grant any extension necessary for entry; Charge any fee due to our Deposit Account No. 24-0037</p>
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(REV. 8-83)US Dept. of Commerce  
PATENT & TRADEMARK OFFICEATTY DOCKET NO.  
118584APPLICATION NO.  
10/776,509

## INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

APPLICANT(S)  
Stuart L. CLAASSEN et al.FILING DATE  
February 12, 2004

GROUP

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
		4,998,288	3/1991	Bui et al.	382	54

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	J. Stein: "Digital Signal Processing, A Computer Science Perspective: Chapter 15 Digital Filter Implementation" 2000 Wiley, XP002331840, ISBN: 0-471-29546-9, pages 584-589; Figures 15.9, 15.10
	D. A. Parker et al.: "Low-Area/Power Parallel FIR Digital Filter Implementations" Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology, Kluwer Academic Publishers, Dordrecht, NL, Vol. 17, No. 1, September 1997 (1997-09), pages 75-92, XP000701964, ISSN: 0922-5773, pages 77-78, Figure 2
	J.H. Arbeiter: "Multidimensional video Image Processing Architecture", Optical Engineering, Soc. of PhotoOptical Instrumentation Engineers. Bellingham, US, Vol. 25, No. 7, July 1986 (198607), pages 875-880, XP000563305, ISSN: 0091-3286, pages 877-878

EXAMINER	DATE CONSIDERED
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Date: July 8, 2005